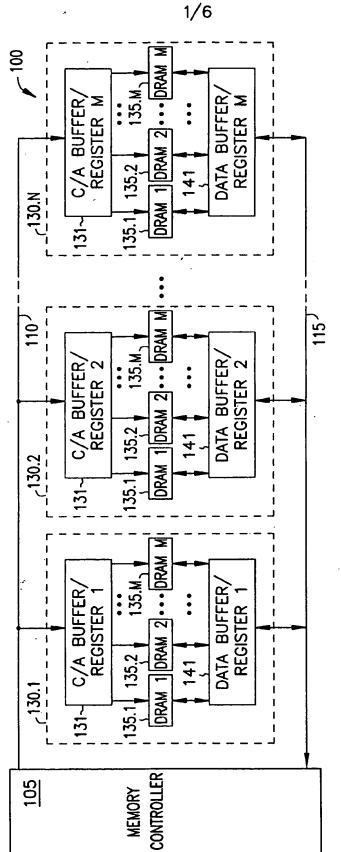
IUILE: PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A UNIDIRECTIONAL COMMAND AND ADDRESS BUS AND A BIDIRECTIONAL DATA BUS INVENTORS NAME: Kevin J. Ryan SERIAL NO.: 09/434,654

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PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A UNIDIRECTIONAL COMMAND AND ADDRESS BUS AND A BIDIRECTIONAL DATA BUS INVENTORS NAME: Kevin J. Ryan SERIAL NO.: 09/434,654 200 FIG. 2 --- DRAM 2 --- DRAM 235.M₂ 235.0 DATA BUFFER/ REGISTER P C/A BUFFER/ REGISTER N BUFFER C/A BUFFER DATA BUFFE REGISTER REGISTER 1 DRAM DRAM 235.2 235.2 241 241 DRAM DRAM 235.1, 231~ 235.1 2 ... DRAM M 215, 210 235.M₂ 235.0. DATA BUFFER/ REGISTER 2 C/A BUFFER/ REGISTER 2 DATA BUFFER/ REGISTER 2 C/A BUFFER REGISTER 1 DRAM DRAM 235.2 235.2 241 241 240.2 DRAM DRAM 231~ 231~ 235.1 2 --- DRAM · DRAM 235.0 235.M DATA BUFFER/ REGISTER 1 C/A BUFFER/ REGISTER 1 DATA BUFFER C/A BUFFER REGISTER REGISTER 1 DRAM 235.2 235.2 241 240.1 230.1 DRAM DRAM 231~ 235.1 231~ 235.1 CONTROLLER

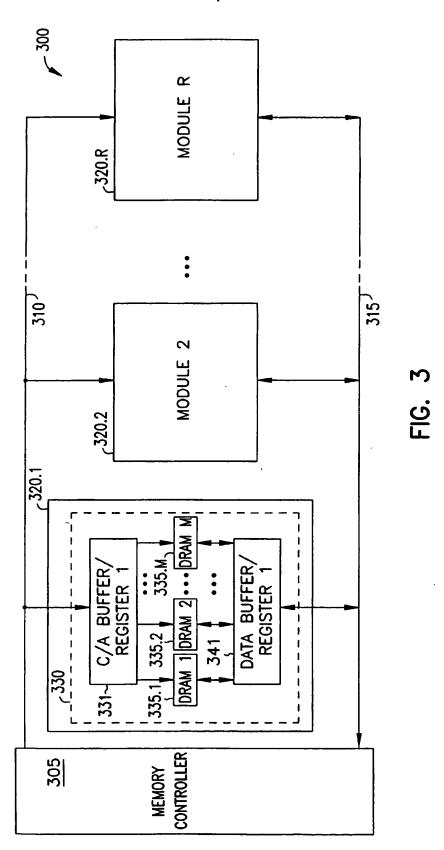
MEMORY

205

TITLE: PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A UNIDIRECTIONAL COMMAND AND ADDRESS BUS AND A BIDIRECTIONAL DATA BUS INVENTORS NAME: Kevin J. Ryan

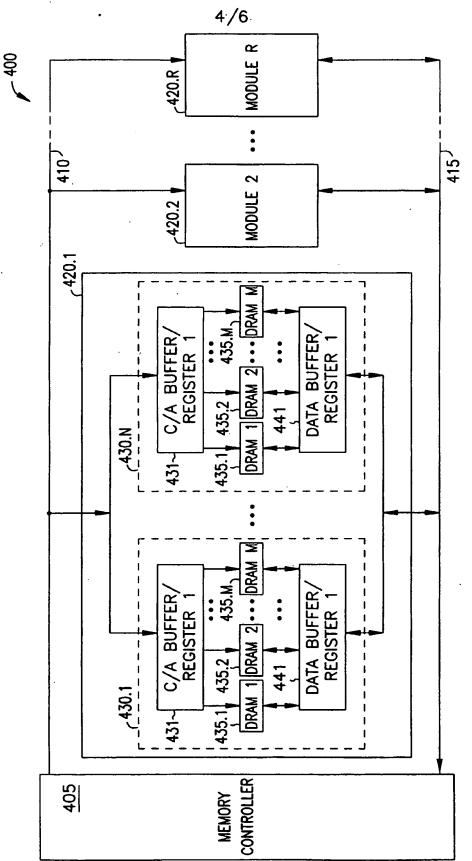
SERIAL NO.: 09/434,654

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PIPELINED PACKET-ORIENTED MEMORY SYSTEM HAVING A UNIDIRECTIONAL COMMAND AND ADDRESS BUS AND A BIDIRECTIONAL DATA BUS INVENTORS NAME: Kevin J. Ryan
SERIAL NO.: 09/434,654





INVENTORS NAME: Kevin J. Ryan SERIAL NO.: 09/434,654

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REPLACEMENT SHEET

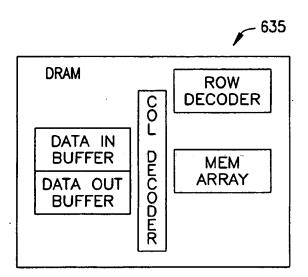


FIG. 6 (Prior Art)